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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/774,700	02/10/2004	Shunpei Yamazaki	12732-211001 / US6966	8592
26171	7590	05/02/2005	EXAMINER	
FISH & RICHARDSON P.C. 1425 K STREET, N.W. 11TH FLOOR WASHINGTON, DC 20005-3500			TRAN, MAI HUONG C	
			ART UNIT	PAPER NUMBER
			2818	

DATE MAILED: 05/02/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/774,700

Applicant(s)

YAMAZAKI ET AL.

Examiner

Mai-Huong Tran

Art Unit

2818

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 09 August 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-26 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-26 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☒ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 10 February 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 7/30/04 & 9/20/04.
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

DETAILED ACTION

Specification

The specification is objected to for the following reasons.

- The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed (see MPEP § 606.01).
- On page 6, line 25, FPC should be spelled out.

Claim Rejections - 35 U.S.C. § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 1-26 are rejected under 35 U.S.C. 102(e) as being anticipated by US Patent Application Publication No. 2003/0231263 to Kato et al.

The applied reference has a common assignee with the instant application. Based upon the earlier effective U.S. filing date of the reference, it constitutes prior art under 35 U.S.C. 102(e). This rejection under 35 U.S.C. 102(e) might be overcome either by a

showing under 37 CFR 1.132 that any invention disclosed but not claimed in the reference was derived from the inventor of this application and is thus not the invention “by another,” or by an appropriate showing under 37 CFR 1.131.

Regarding to claim 1, Kato discloses a semiconductor device comprising a substrate 400; a pixel portion 401 over the substrate 400; a driving circuit 402, 403, 404 over the substrate; and an integrated circuit 415, 414, 413 at least partially overlapped the driving circuit 402, 403, 404 (page 4, [0076]-0082], and figs. 1A-1C).

Regarding to claims 2, 8, 15, and 21, the semiconductor device wherein the integrated circuit includes any one of a display controller, a frame memory, a power source circuit, a CPU, and a memory (page 6, [0104], [0109], page 14, [0223]).

Regarding to claims 3, 10, 16, and 23, the semiconductor device wherein the pixel portion includes any one of a light-emitting element and a liquid crystal element (page 5, [0093]).

Regarding to claims 4, 11, 17, and 24, the semiconductor device wherein the pixel portion includes a light-emitting element having a first electrode, an electroluminescent layer and a second electrode, and wherein the first electrode and the second electrode have the translucency (page 4, [0084]).

Regarding to claims 5, 12, 18, and 25, the semiconductor device wherein the semiconductor device is any one of an active matrix type semiconductor device and a passive matrix type semiconductor device (page 4, [0076]).

Regarding to claims 6,13,19, and 26, Kato discloses an electronic device comprising the semiconductor device wherein the electronic device is any one of a group comprising a display device, a note type personal computer, a mobile computer, a player with a recording medium, an electronic book, a video camera, a portable telephone, a digital camera, a head-mount display, a car navigation, a projector and a car stereo (figs. 21A-21G).

Regarding to claims 7 and 20, Kato discloses a semiconductor device comprising a first substrate 400; a pixel portion 401 over the first substrate; a driving circuit 402, 403, 404 over the first substrate; and an integrated circuit 413, 414, 415 at least partially overlapped the driving circuit wherein the integrated circuit is formed by transcribing an element formation layer formed over a second substrate 406 (page 4, [0076]-[0078]).

Regarding to claims 9 and 22, the semiconductor device, wherein the integrated circuit is formed by laminating the integrated circuit in one layer or two or more layers (figs. 1B-1C).


Regarding to claim 14, Kato discloses a semiconductor device comprising a substrate 400; a pixel portion 401 over the substrate; a driving circuit 402, 403, 404 over the substrate; and an integrated circuit 413, 414, 415 at least partially overlapped the driving circuit wherein the integrated circuit is electrically connected to part or an entirety of the pixel portion and the driving circuit (page 4, [0076]-[0082], and figs. 1A-1C).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Mai-Huong Tran whose telephone number is (571)272-1796. The examiner can normally be reached on 8:00-4:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David Nelms can be reached on (571)272-1787. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

A handwritten signature in black ink, appearing to be 'JCH'.A handwritten signature in black ink, appearing to be 'Mai-Huong Tran'.

Mai-Huong Tran
Examiner
Art Unit 2818